



30V N-CHANNEL ENHANCEMENT MODE MOSFET WITH SCHOTTKY DIODE

PowerDI3333-8

Product Summary

V _{(BR)DSS}	R _{DS(ON)}	Package	I _D T _A = +25°C
30V	10mΩ @ V _{GS} = 10V	PowerDI [®] 3333-8	12A
300	15mΩ @ V _{GS} = 4.5V	PowerDI 3333-8	9.5A

Description

This MOSFET has been designed to minimize the on-state resistance (R_{DS(on)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

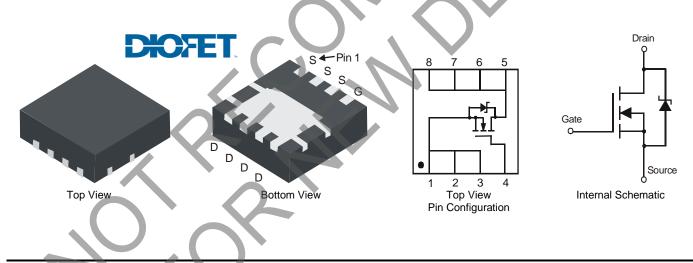
- Backlighting
- **Power Management Functions**
- **DC-DC Converters**

Features

- DIOFET utilizes a unique patented process to monolithically integrate a MOSFET and a Schottky in a single die to deliver:
 - Low R_{DS(ON)} minimize conduction losses
 - Low $V_{\mbox{\scriptsize SD}}$ reducing the losses due to body diode conduction
 - Low Q_{rr} lower Q_{rr} of the integrated Schottky reduces body diode switching losses
 - Low gate capacitance (Qg/Qgs) ratio reduces risk of shootthrough or cross conduction currents at high frequencies
- Small form factor thermally efficient package enables higher density end products
- Occupies just 33% of the board area occupied by SO-8 enabling smaller end product
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: PowerDI3333-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See diagram
- Terminals: Finish Matte Tin annealed over Copper leadframe.
- Solderable per MIL-STD-202, Method 208 (e3)
- Weight: 0.072 grams (approximate)



Ordering Information (Note 4)

	Part Number	Case	Packaging
	DMG7702SFG-7	PowerDI3333-8	2000/Tape & Reel
	DMG7702SFG-13	PowerDI3333-8	3000/Tape & Reel
Notes:	1. No purposely added lead. Fully EU Dire	ctive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/	863/EU (RoHS 3) compliant.

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2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information



G72 = Product Type Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 11 = 2011) WW = Week Code (01 to 53)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	30	V
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 6) // 10//	Steady State	T _A = +25°C T _A = +70°C	lo	12 9.5	А
Continuous Drain Current (Note 6) $V_{GS} = 10V$	t<10s	T _A = +25°C T _A = +70°C	Ŀ	16.0 12.7	A
Continuous Dusin Cument (Note C) // 4 EV	Steady State	T _A = +25°C T _A = +70°C	۱ _D	9.5 7.5	A
Continuous Drain Current (Note 6) V _{GS} = 4.5V	t<10s	T _A = +25°C T _A = +70°C	lo	13.0 10.3	А
Pulsed Drain Current (10µs pulse, duty cycle = 1%)			IDM	90	A
Maximum Continuous Body Diode Forward Current (Note 6)		Is	3.5	A
Avalanche Current (Note 7) L = 0.1mH			I _{AR}	17	A
Repetitive Avalanche Energy (Note 7) L = 0.1mH			EAR	43	mJ

Thermal Characteristics

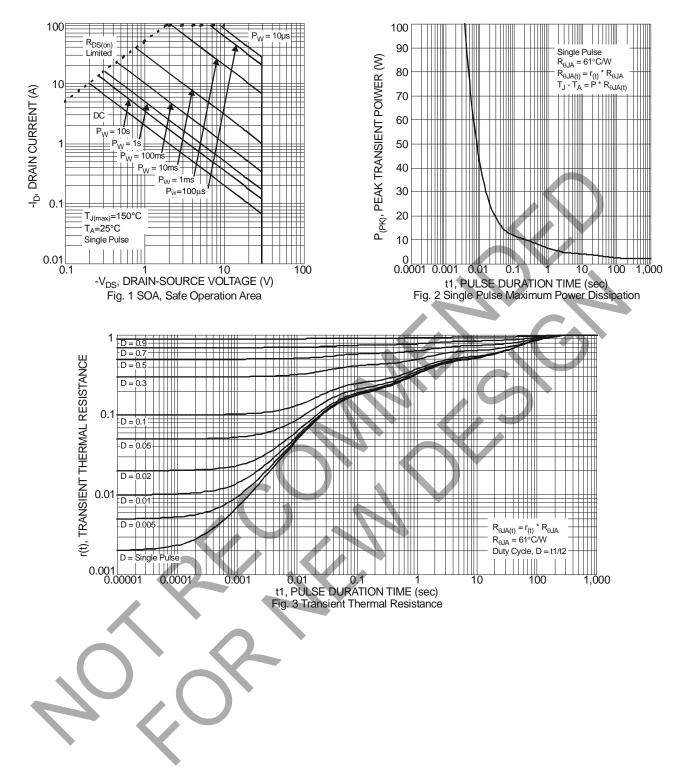
Characteristic		Symbol	Value	Unit
Total Dawar Dissingtion (Note 5)	T _A = +25°C	D	0.89	W
Total Power Dissipation (Note 5)	T _A = +70°C	PD	0.55	vv
Thermal Resistance, Junction to Ambient (Note 5)	Steady state	P	145	°C/W
memai Resistance, Junction to Ambient (Note 5)	t<10s	$R_{ extsf{ heta}JA}$	74	C/W
Total Power Dissipation (Note 6)	T _A = +25°C	Р	2.2	W
Total Power Dissipation (Note 6)	T _A = +70°C	PD	1.3	vv
Thermal Desigtance, Junction to Ambient (Note C)		Р	58	
Thermal Resistance, Junction to Ambient (Note 6)	t<10s	$R_{ extsf{ heta}JA}$	31	°C/W
Thermal Resistance, Junction to Case (Note 6)		R ₀ JC	11	
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout. 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate. 7. J_{AR} and E_{AR} ratings are based on low frequency and duty cycles to keep $T_J = +25^{\circ}C$ Notes:



NOT RECOMMENDED FOR NEW DESIGN -NO ALTERNATE PART

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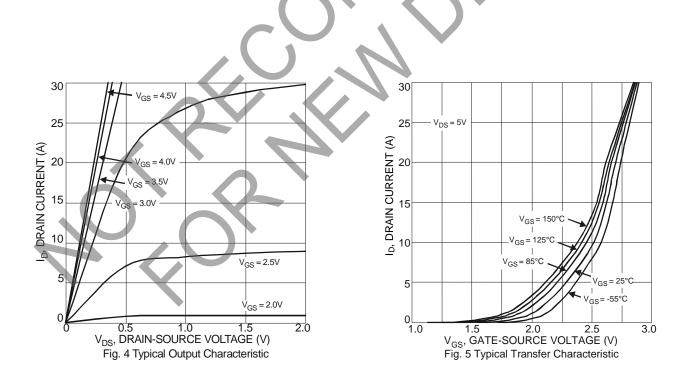




Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						·
Drain-Source Breakdown Voltage	BV _{DSS}	30	-	-	V	$V_{GS} = 0V, I_D = 250 \mu A$
Zero Gate Voltage Drain Current	IDSS	-	-	100	μA	$V_{DS} = 30V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	-	-	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(th)}	1.0	1.5	2.5	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
Static Drain-Source On-Resistance	р	-	7.3	10	mΩ	$V_{GS} = 10V, I_D = 13.5A$
	R _{DS (ON)}	-	10	15	11152	V _{GS} = 4.5V, I _D = 11A
Forward Transfer Admittance	Y _{fs}	-	22	-	S	V _{DS} = 5V, I _D = 10.0A
Diode Forward Voltage	V _{SD}	-	0.45	0.55	V	$V_{GS} = 0V, I_{S} = 1A$
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	Ciss	-	1296	4310	pF	
Output Capacitance	Coss	-	415	-	pF	$V_{DS} = 15V, V_{GS} = 0V,$ f = 1.0MHz
Reverse Transfer Capacitance	Crss	-	204	-	pF	1 = 1.00112
Gate Resistance	Rg	0.26	1.6	2.7	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$
Total Gate Charge V _{GS} = 4.5V	Qg	-	14.7	-	nC	
Total Gate Charge V _{GS} = 10V	Qg	-	31.6	-	nC	
Gate-Source Charge	Q _{gs}	-	3.5		nC	$V_{DS} = 15V, V_{GS} = 10V, I_D = 13.5A$
Gate-Drain Charge	Q _{qd}	-	5.0	-	nC	
Turn-On Delay Time	t _{D(on)}	-	15.8	-	ns	
Turn-On Rise Time	tr	-	27.8		ns	$V_{GS} = 10V, V_{DS} = 15V,$
Turn-Off Delay Time	t _{D(off)}	-	29.7	-	ns	$R_{G} = 3\Omega, I_{D} = 8.8A$
Turn-Off Fall Time	tf	-	13.6		ns	
Reverse Recovery Time	t _{rr}		13.1	(ns	I _F = 13.5A, di/dt = 100A/μs
Reverse Recovery Charge	Q _{rr}	-	4.3	-	nC	I _F = 13.5A, di/dt = 100A/μs

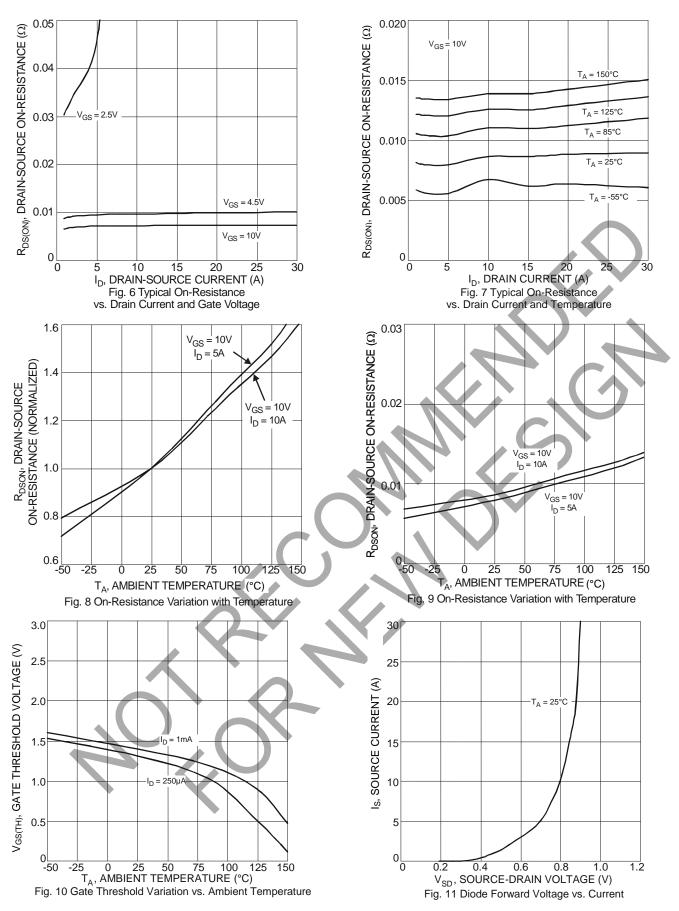
Notes: 8. Short duration pulse test used to minimize self-heating effect. 9. Guaranteed by design. Not subject to product testing.





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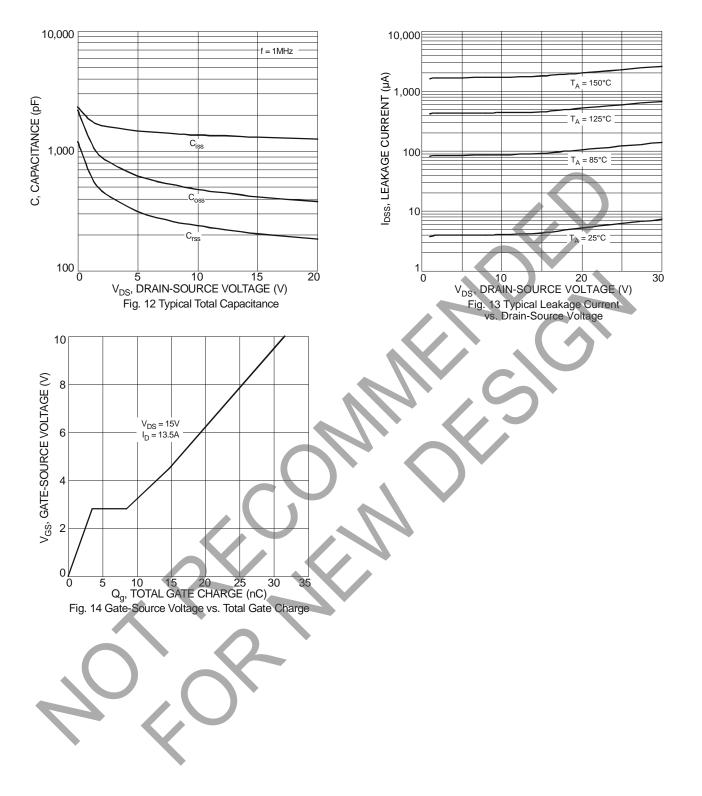
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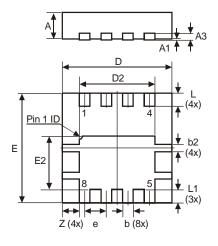
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Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



	PowerDI3333-8						
Dim	Min	Max	Тур				
D	3.25	3.35	3.30				
Е	3.25	3.35	3.30				
D2	2.22	2.32	2.27				
E2	1.56	1.66	1.61				
Α	0.75	0.85	0.80				
A1	0	0.05	0.02				
A3	-	-	0.203				
b	0.27	0.37	0.32				
b2	-		0.20				
L	0.35	0.45	0.40				
L1	-	-	0.39				
е	-	-	0.65				
Ζ			0.515				
	Dimens	sions ir	mm				

Value (in mm)

0.650

0.230 0.420

3.700

2.250

1.850

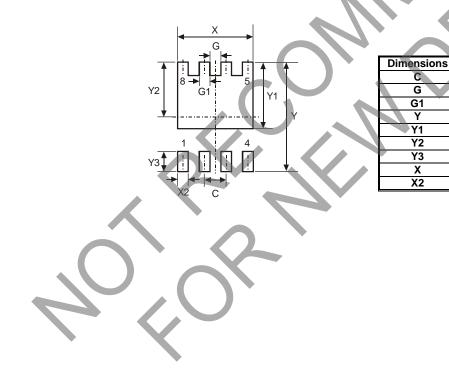
0.700

2.370

0.420

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version





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